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CLAIMS

1. An oscillator circuit (1), at least comprising
at least one oscillator device (100) with
5 at least one oscillator bias contact (Vcm)
and
a bias source (Vbias) having a source contact connected to said oscillator bias
contact,
wherein
10 said bias source includes a varying bias source which in use provides a bias
varying in time.
2. An oscillator circuit (1) as claimed in claim 1, wherein said bias source is
a switched DC source which in use provides a bias signal varying between a
15 first level and a second level.
3. An oscillator circuit (1) as claimed in claims 1 or 2, further comprising a
signal shaper device (101) connected to an oscillator output contact of said
oscillator device.
20
4. An oscillator circuit (1) as claimed in claim 3, wherein said signal shaper
device comprises at least one limiter device (101).
5. An oscillator circuit (1) as claimed in claim 3, wherein said signal shaper
25 device comprises a band-pass filter device.
6. An oscillator circuit (1) as claimed in any one of the preceding claims,
further comprising:
a bias control circuit (Meas,Tr,S) for switching the bias source (Vbias) on and
30 off depending on a signal outputted by the oscillator device (100).

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7. An oscillator circuit (1) as claimed in any one of the preceding claims, wherein said oscillator device (100) at least comprises at least one electrical device with a positive feedback loop.
- 5 8. An oscillator circuit (1) as claimed in any one of the preceding claims, wherein said oscillator device (100) at least comprises at least one resonator body.
- 10 9. An oscillator circuit (1) as claimed in any one of the preceding claims, further comprising a negative resistance device (102) at least comprising at least one transistor device (Mresa, Mresb).
- 15 10. An oscillator circuit (1) as claimed in claim 4, wherein said limiter (101) at least comprises at least one differential amplifier (Mlima, Mlimb) with: at least one input contact connected to at least one oscillator output contact (inp), and at least one output contact (outp) connected to a load.
- 20 11. An oscillator circuit (1) as claimed in claim 10, wherein said load comprises: at least one resistor (Rlima) connecting at least one of said at least one output contacts (outp) to a power supply (Vcc).
- 25 12. An oscillator circuit (1) as claimed in any one of the claims 4-11, wherein said limiter (101) at least comprises at least one transistor device (Mlima, Mlimb).
- 30 13. An oscillator circuit as claimed in any one of the preceding claims, wherein said bias source comprises a bias voltage source.

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14. An oscillator circuit as claimed in any one of the preceding claims, wherein said bias source comprises a bias current source.

- 5 15. An oscillator biasing method, at least comprising:
applying a bias over a first oscillator bias contact (V_{cm});
switching said bias off if a predetermined first criterion is satisfied and
switching said bias on if a predetermined second criterion is satisfied.